

MILTON RIBEIRO

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SUMMARY OF EXPERIENCE AND QUALIFICATIONS

- Self starter. Love to start new teams.
- Experienced evaluating new companies and technologies.
- Design of wireless handheld devices: system architecture, low power hardware/software design, design for low-cost manufacturing.
- Embedded software design: Windows CE and real-time kernels.
- Microcontroller firmware design: Microchip PIC and Philips 8051 lines.
- Systems-on-silicon (SOC) architecture and verification.
- Digital signal processing (DSP) algorithms: architecture, design and implementation in both hardware and software (DSP and general purpose microprocessors).
- Windows CE system software development (from CE 1.0 all the way to CE.net).

EMPLOYMENT

Fyre Storm, Inc., Sunnyvale, California

Sr. Director, Advanced Architecture Group

Product Line Manager

Technical Advisor

From 08/2002 to the present

Started my contribution to the company as a Technical Advisor. Used my deep knowledge of PDAs and other handheld devices as a basis for specifying Fyre Storm's first generation Power Management Controller.

Since March of 2003, became Fyre Storm's Product Line Manager for PDAs, Converged Devices and Digital Still Cameras. That has involved the development of the Product Roadmap of the company and the Marketing Requirements Document of its first part, detailing customer requirements and Marketing plans.

Since August of 2003, took over the Advanced Architecture Group, developing model predictive control algorithms and computer architectures for the power supply control field.

Main accomplishments:

- Developed customer product requirements for the company's first product line: PDAs, Converged Devices and Digital Still Cameras.
- Wrote the MRD for the company's first product.
- Worked together with the company's early adopters to understand their requirements and develop an optimum solution to their needs.
- Prepared the detailed marketing presentation used during the company's series B funding round.
- Developed from scratch the 1st generation model predictive control algorithms of the company.
- Built the Advanced Architecture Group into a dynamic 5 people 1st-rate R&D team.

bSQUARE Corporation – Silicon Valley Division, Sunnyvale, California

From 05/2000 to 02/2003

Chief Scientist

Chief Architect

After the acquisition, Mainbrace became bSQUARE's Silicon Valley Division. My role expanded to include the leading of all the company's hardware platforms, besides advising executive management on wireless and handheld technology.

Main accomplishments:

- Architected and led the development of PXA250 vertical market hardware and software platforms.
- Architected and led the development of bSQUARE's Power Handheld PDA (currently for sale in the UK by Vodaphone) . That included from design concept, all the way to manufacturing in China.

Mainbrace Corporation, Sunnyvale, California

Co-Founder

Chief Technology Officer

Vice-President of Engineering

From 02/1998 to 05/2000

Joined Mainbrace, a Windows CE technology development company, as its Chief Technology Officer, applying my background in silicon and system architecture to the platforms developed by the company. Mainbrace was acquired by bSQUARE Corporation of Bellevue, WA, in a \$32 million merger.

Main accomplishments:

- Created and grew Mainbrace's hardware engineering team.
- Grew the engineering group to 25+ engineers.
- Led the development of all Mainbrace's hardware and software platforms, including those based on ARM7200, Mips 3900 and SA1110.
- Led the development of end-user products developed by Mainbrace on the OEM's behalf, such as PDA, eBook and Web-telephone.
- Worked closely with Marketing and sales in the revenue growth of the company, from \$800 thousand in the first year of operation to \$4.5 million on the year the company was acquired.

Philips Semiconductors - Handheld Computing Group, Sunnyvale, California

Systems Development Group Manager

From 02/1997 to 02/1998

Part of the management team of the Handheld Computing Group, responsible for processor architecture, device specification and validation, applications support and technical marketing.

Main accomplishments:

- Grew the engineering group from under 10 members to an 18-engineer strong organization.
- Led the specification, development, validation and technical market introduction of the 2nd and 3rd generations of Philips Semiconductors chipsets for Windows CE.
- Worked closely with the Marketing team during the design win process of Sharp, Compaq and Samsung.
- Personally led the co-development of the Mobilon HPC with Sharp in Japan, leading to the award of the prestigious MITI prize to both companies for the best international cooperation between a Japanese and a non-Japanese company in 1997.

Lead Engineer

From 04/1995 to 02/1997

Chief Architect of the Poseidon/Betty (PR31500/UCB1200) chipset for Windows CE-based handheld PCs. Lead a small engineering team during the specification, design and system validation of that chipset. Worked closely with Physical IC designers of Toshiba in Japan, during the validation of the design of Poseidon, and in the Netherlands during the validation of the design of Betty. Active member of the Philips Velo design team.

Main accomplishments:

- High integration, high performance and low cost of the Poseidon/Betty chipset were key ingredients on the great success enjoyed by Velo, certainly the best 1st generation Windows CE Handheld PC.
- The success of Velo and the qualities of the Poseidon/Betty chipset catapulted Philips Semiconductors into the leadership spot of the integrated processor market for Windows CE.
- Worked closely with the Marketing team during the design win process of Sharp, Compaq and Samsung.

Philips Advanced Communication Enterprise, Eindhoven, The Netherlands

System Architect

From 01/1994 to 04/1995

Architected and developed intelligent network applications based in General Magic's Telescript technology. Also managed the pilot deployment project of that technology in Spain, with Telefónica.

Main accomplishments:

- Developed close technical and business relationship with Telefónica's Advanced Development Group, resulting in the decision of Telefónica to deploy a pilot network based on that technology.

Philips Research Laboratories - Digital Magnetic Recording Group

Eindhoven, The Netherlands

Research Scientist

From 01/1991 to 12/1993

Developed digital signal processing algorithms for the data recovery sub-system of the Digital Compact Cassette. Research included the development digital feedback equalizers and clock-recovery schemes for magnetic recording channels.

Main accomplishments:

- Developed algorithms made into the 2nd and 3rd generations of the product, resulting in a more robust and less power hungry design.
- Produced a number of papers for international publications, and contributed to two patents in the area of modulation codes for digital recorders.

EDUCATION

Master of Electronics Engineering (MEE)

Eindhoven International Institute (EII)

Technische Universiteit Eindhoven (TUE)

Eindhoven, The Netherlands

From 01/1990 to 05/1991

Specialized in digital signal processing, and data recovery applied to digital magnetic recording. Included summer internship at the Magnetic Recording Group of the Philips Research Laboratories, in Eindhoven, The Netherlands.

Engenheiro Eletrônico (equivalent to 5-year BSEE)

Instituto Tecnológico de Aeronáutica (ITA)

São José dos Campos, SP, Brazil

From 02/1985 to 12/1989

Specialized in the digital signal processing and telecommunications systems.

PATENTS

Milton Ribeiro et al., *Apparatus for decoding a channel signal into an information signal and reproducing arrangement provided with the apparatus*, US Patent 6,038,274 (European Patent 0797826B1 and World Patent 97/14150), March 2000.

Milton Ribeiro et al., *Transmission and Reception of Digital Information Signal*, European Patent Application 0655850A3, October 1994.

OTHERS

Languages:

English: Fluent (written and spoken)

Portuguese: Fluent (native tongue)

Dutch: Fluent (written and spoken)

Spanish: Able to communicate

Immigration status:

- Permanent resident of the United States of America.
- Holds both Brazilian and Dutch (European Union) citizenships.

REFERENCES

Available under request.